WEST Search History

Hide Items | Restore | Clear | Cancel

DATE: Saturday, January 03, 2004

Hide?	Set Name	Query	Hit Count
		DB=PGPB,USPT,JPAB; PLUR=YES; OP=OR	
☐	L18	L16 and (debug same event)	15
☐	L17	L16 and debug and event	55
☐	L16	L12 and (time same multiplex$)	1284
☐	L15	L14 and (time same multiplex$)	26
☐	L14	L13 and debug$ and event	42
☐	L13	L12 and mode and suspen$	153
☐	L12	scan same register same multiplex$	2020
☐	L11	L9 and (quantum or period or variance)	35
☐	L10	L9 and quantum	0
☐	L9	L8 and target and host	40
☐	L8	L3 and timing	89
☐	L7	L1 and (timing same consisten$)	6
☐	L6	L5 and (timing same consisten$)	2
☐	L5	L4 and target and host	42
☐	L4	L3 and (dynamic or ((real or run) same time))	111
☐	L3	11 and L2	142
☐	L2	speed same (variance or change)	204027
☐	L1	emulat$ same speed same processor	701

END OF SEARCH HISTORY

WEST Search History

DATE: Tuesday, February 25, 2003

Set Name	Query	Hit Count	Set Name
side by side			result set

DB=USPT,PGPB,JPAB; PLUR=YES; OP=OR

L12	L11 and debug$	51	L12
L11	L10 and ((in same circuit same emulat$) or ice)	54	L11
L10	L9 and (event same interrupt)	68	L10
L9	l7 and L8	97	L9
L8	suspen$ same execut$	9609	L8
L7	emulation same monitor$	876	L7
L6	L3 and interrupt	5	L6
L5	L3 and event and interrupt	3	L5
L4	L3 and hardware and interrupt	3	L4
L3	emulation same monitor$ same suspen$4	7	L3
L2	((((((hardware same suspen$4)and interrupt and emulat$)and (embedded or (in same circuit)))and (subsequen$ or precedence))and (emulation same monitor$)) and software)	43	L2
L1	((((((hardware same suspen$4)and interrupt and emulat$)and (embedded or (in same circuit)))and (subsequen$ or precedence))and (emulation same monitor$))and software) not (((((hardware same suspen$4)and interrupt and emulat$)and (embedded or (in same circuit)))and (subsequen$ or precedence))and (emulation same monitor$))and window$4))	4	L1

END OF SEARCH HISTORY

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Search Results - Record(s) 1 through 15 of 51 returned.

☐ 1. Document ID: US 20020052729 A1

L12: Entry 1 of 51   File: PGPB   May 2, 2002

PGPUB-DOCUMENT-NUMBER: 20020052729
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20020052729 A1

TITLE: Apparatus and method for verifying a logic function of a semiconductor chip

PUBLICATION-DATE: May 2, 2002

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Kyung, Chong Min	Daejeon-si		KR	
Ki, Ando	Daejeon-si		KR	
Lee, Seung Jong	Goyang-si		KR	
Jeon, Young Wook	Daejeon-si		KR	

US-CL-CURRENT: 703/28

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC |
| Draw Desc | Image |

☐ 2. Document ID: US 20020013918 A1

L12: Entry 2 of 51   File: PGPB   Jan 31, 2002

PGPUB-DOCUMENT-NUMBER: 20020013918
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20020013918 A1

TITLE: Devices, systems and methods for mode driven stops

PUBLICATION-DATE: January 31, 2002

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Swoboda, Gary L.	Sugar Land	TX	US	
Daniels, Martin D.	Houston	TX	US	

US-CL-CURRENT: 714/30; 712/227

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC |
| Draw Desc | Image |

◻ 3.   Document ID: US 6522985 B1

L12: Entry 3 of 51   File: USPT   Feb 18, 2003

US-PAT-NO: 6522985
DOCUMENT-IDENTIFIER: US 6522985 B1

TITLE: Emulation devices, systems and methods utilizing state machines

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC |
| Draw Desc | Image |

◻ 4.   Document ID: US 6510507 B1

L12: Entry 4 of 51   File: USPT   Jan 21, 2003

US-PAT-NO: 6510507
DOCUMENT-IDENTIFIER: US 6510507 B1

TITLE: Page address look-up range ram

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC |
| Draw Desc | Image |

◻ 5.   Document ID: US 6421754 B1

L12: Entry 5 of 51   File: USPT   Jul 16, 2002

US-PAT-NO: 6421754
DOCUMENT-IDENTIFIER: US 6421754 B1

TITLE: System management mode circuits, systems and methods

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC |
| Draw Desc | Image |

◻ 6.   Document ID: US 6349392 B1

L12: Entry 6 of 51   .   File: USPT   Feb 19, 2002

US-PAT-NO: 6349392
DOCUMENT-IDENTIFIER: US 6349392 B1

TITLE: Devices, systems and methods for mode driven stops

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | | KWIC |
| Draw Desc | Image |

◻ 7.   Document ID: US 6112298 A

L12: Entry 7 of 51   File: USPT   Aug 29, 2000

US-PAT-NO: 6112298
DOCUMENT-IDENTIFIER: US 6112298 A

TITLE: Method for managing an instruction execution pipeline during debugging of a
data processing system

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | | KWIC |
| Draw Desc | Image |

☐ 8. Document ID: US 6112273 A

L12: Entry 8 of 51   File: USPT   Aug 29, 2000

US-PAT-NO: 6112273
DOCUMENT-IDENTIFIER: US 6112273 A

TITLE: Method and apparatus for handling system management interrupts (SMI) as well
as, ordinary interrupts of peripherals such as PCMCIA cards

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | | KWIC |
| Draw Desc | Image |

☐ 9. Document ID: US 6094729 A

L12: Entry 9 of 51   File: USPT   Jul 25, 2000

US-PAT-NO: 6094729
DOCUMENT-IDENTIFIER: US 6094729 A

TITLE: Debug interface including a compact trace record storage

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | | KWIC |
| Draw Desc | Image |

☐ 10. Document ID: US 6085336 A

L12: Entry 10 of 51   File: USPT   Jul 4, 2000

US-PAT-NO: 6085336
DOCUMENT-IDENTIFIER: US 6085336 A

TITLE: Data processing devices, systems and methods with mode driven stops

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | | KWIC |
| Draw Desc | Image |

☐ 11. Document ID: US 6081885 A

L12: Entry 11 of 51   File: USPT   Jun 27, 2000

US-PAT-NO: 6081885

DOCUMENT-IDENTIFIER: US 6081885 A

TITLE: Method and apparatus for halting a processor and providing state visibility on a pipeline phase basis

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments		KWIC
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☐ 12. Document ID: US 6075941 A

L12: Entry 12 of 51   File: USPT   Jun 13, 2000

US-PAT-NO: 6075941
DOCUMENT-IDENTIFIER: US 6075941 A

TITLE: Microcomputer

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments		KWIC
Draw Desc	Image										

☐ 13. Document ID: US 6065106 A

L12: Entry 13 of 51   File: USPT   May 16, 2000

US-PAT-NO: 6065106
DOCUMENT-IDENTIFIER: US 6065106 A

TITLE: Resuming normal execution by restoring without refetching instructions in multi-word instruction register interrupted by debug instructions loading and processing

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments		KWIC
Draw Desc	Image										

☐ 14. Document ID: US 6063131 A

L12: Entry 14 of 51   File: USPT   May 16, 2000

US-PAT-NO: 6063131
DOCUMENT-IDENTIFIER: US 6063131 A

TITLE: Emulator system and emulation method

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments		KWIC
Draw Desc	Image										

☐ 15. Document ID: US 6055649 A

L12: Entry 15 of 51   File: USPT   Apr 25, 2000

US-PAT-NO: 6055649
DOCUMENT-IDENTIFIER: US 6055649 A

TITLE: Processor test port with scan chains and data streaming

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | KWIC |

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Terms	Documents
L11 and debug$	51

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